

Item no.: UPS-APLC2F-A20-0232

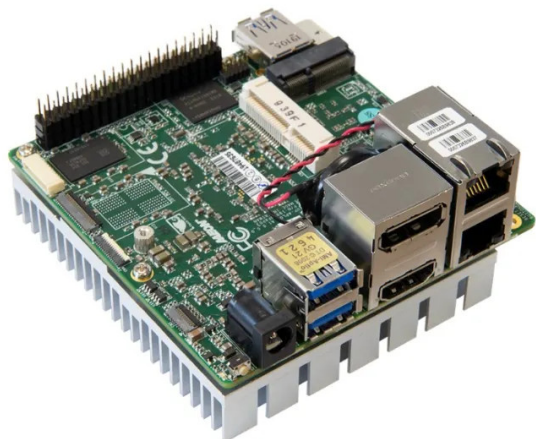
UPS-APLC2F-A20-0232 - UP Squared Celeron Duo Core 02/32

from **221,95 EUR**

Item no.: UPS-APLC2F-A20-0232

shipping weight: 0.30 kg

Manufacturer: UP Bridge the Gap



Product Description

UPS-APLC2F-A20-0232 - UP Squared Celeron Duo Core 02/32

UP Squared, the first x86 computing board with FPGA onboard, 2 GB RAM, 32 GB eMMC.

Inheriting the 40-pin GP bus from the first generation of UP, UP Squared brings more I/O interfaces and CPU power.

System level

- SoC: Intel(R) Celeron(TM) N3350 (up to 2.4 GHz)
- # of cores: 2
- Graphics: Intel(R) HD Graphics 500
- VPU: Optional (via mPCIe)
- FPGA: Intel(R) FPGA Altera MAX 10
- Memory: 2 GB
- eMMC: 32 GB
- BIOS: UEFI
- RTC: Yes
- Wake on LAN (WoL): Yes
- Watchdog timer: Yes
- PXE: Yes
- Compatible OS: Microsoft Windows 10 full version, Linux (ubinux, Ubuntu, Yocto), Android
- Power requirement: 5 V DC-in (at) 6 A, 5.5/2.1 mm jack
- Power supply type: AT (default)/ATX
- Power consumption (typical): 18 Watt
- Dimensions: 3.4" x 3.54" (86.5 x 90 mm)
- Operation temperature: 32°F - 140°F (0°C - 60°C)
- Storage temperature: -40°F - 176°F (-40°C - 80°C)
- Operation humidity: 0% - 90% relative humidity, non-condensing
- MTBF (hours): 173,446 hours
- Certification: CE/FCC Class A

Display

- HDMI: 1 (HDMI 1.4b)
- Display port: 1 (DP 1.2 4K (at) 60 Hz)
- MIPI DSI/eDP: 1x eDP

I/O

- Audio: I2S audio port
- USB 2.0 type A: 2
- USB 2.0 pin header: 2
- USB 3.0 Type A: 3
- USB 3.0 OTG Micro B: 1
- UART: 2x UART (Tx/Rx) debug port (pin header)
- HDD interface: 1x SATA 3.0
- Expansion slot: 1x mPCIe, 1x M.2 2230 E key, 1x 60-pin EXHAT
- 40-pin GP bus: 1
- MIPI-CSI: 1x MIPI-CSI 2 lane, 1x MIPI-CSI 4 lane
- SIM slot: No

Connectivity

- Ethernet: 2x GbLAN (RealTek RTL8111H-CG)
- WiFi: Optional (via. M.2 2230)
- Bluetooth: Optional (via. M.2 2230)
- LTE/4G: Optional (via. mPCIe)

Cooling

- Cooling: Passive cooling (fanless)

Specifications

**Scan this QR code to
view the product**
All details, up-to-date
prices and availability

